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APPLICATION NO.	_ F	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/852,009	52,009 05/07/2001		Coke S. Reed	M-3240-3D US	4824
24251	7590	03/04/2005		EXAM	INER
SKJERVE		LILL LLP	NGUYEN, BRIAN D		
25 METRO SUITE 700				ART UNIT	PAPER NUMBER
SAN JOSE, CA 95110			• •	2661	
				DATE MAILED: 03/04/2009	DATE MAILED: 03/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)
Office Action Summer.	09/852,009	REED, COKE S.
Office Action Summary	Examiner	Art Unit
	Brian D Nguyen	2661
The MAILING DATE of this communication a Period for Reply	appears on the cover sheet w	ith the correspondence address
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATIOI - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a relative to reply within the set or extended period for reply will, by stated any reply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a reply within the statutory minimum of thir od will apply and will expire SIX (6) MON tute, cause the application to become Al	reply be timely filed ty (30) days will be considered timely. ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).
Status		
1)⊠ Responsive to communication(s) filed on the 2a) This action is FINAL . 2b) This action is FINAL . 2b) This action is application is in condition for allow closed in accordance with the practice under the practi	his action is non-final. wance except for formal matt	•
Disposition of Claims		
4) ☐ Claim(s) 39-116 is/are pending in the applic 4a) Of the above claim(s) is/are withd 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 39-78,85,88,92 and 98-116 is/are r 7) ☐ Claim(s) 79-84,86,87,89-91 and 93-97 is/are 8) ☐ Claim(s) are subject to restriction and Application Papers	rawn from consideration. rejected. e objected to. d/or election requirement.	-
9) The specification is objected to by the Exami		ated to be the Francisco
10) ☐ The drawing(s) filed on <u>07 May 2001</u> is/are: Applicant may not request that any objection to the state of the s	· · · · · · · · · · · · · · · · · · ·	· · · · · · · · · · · · · · · · · · ·
Replacement drawing sheet(s) including the corr	ection is required if the drawing	(s) is objected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for forei a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure * See the attached detailed Office action for a li	ents have been received. ents have been received in A riority documents have been eau (PCT Rule 17.2(a)).	application No received in this National Stage
Attachment(s) 1) Notice of References Cited (PTO-892)	∆ □ 1	Summon (BTO 412)
 Notice of References Cited (PTO-692) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date <u>5/7/01</u>. 	Paper No(s	Summary (PTO-413) s)/Mail Date nformal Patent Application (PTO-152)

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DETAILED ACTION

Specification

1. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Objections

2. Claims 39-67, 69, 74, 76, and 78-116 are objected to because of the following informalities:

Claim 39, "device set A" in lines 6 and 18 seem to refer back to "a device set A" in line 6. If this is true, it is suggested to change "device set A" in lines 6 and 18 to --the device set A--. "device set Z" in line 8 seems to refer back to "a device set Z" in line 8. If this is true, it is suggested to change "device set Z" in line 8 to --the device set Z--. "device set Z(CX_H)" in line 25 seems to refer back to "a device set Z(CX_H)" in line 25. If this is true, it is suggested to change "device set Z(CX_H)" in line 25 to --the device set Z(CX_H)--. In line 17, "the path R is **in** a member of the sequence cx" seems to be a typographical error. If this is true, it is suggested to delete "in". In line 22, "the device plurality Z" seems to refer back to "a device set Z" in line 8. If this is true, it is suggested to change "the device plurality Z" to --the device set Z--.

Claim 40, "paths P(x)" in line 3 seems to refer back to "paths P(x)" in line 29 of claim 39. If this is true, it is suggested to change "paths P(x)" in line 3 to --the paths P(x)--.

Claim 44, "device set Z(D)" in line 11 seems to refer back to "device set Z(D)" in line 9. If this is true, it is suggested to change "device set Z(D)" to --the device set Z(D)--.

Claim 46, "node q" in line 5 seems to refer back to "node q" in line 27 of claim 39. If this is true, it is suggested to change "node q" to --the node q--.

Claim 51, "device set A" in lines 6 and 18 seem to refer back to "a device set A" in line 6. If this is true, it is suggested to change "device set A" in lines 6 and 18 to --the device set A--. "device set Z" in line 8 seems to refer back to "a device set Z" in line 8. If this is true, it is suggested to change "device set Z" in line 8 to --the device set Z--. "device set Z(CX_H)" in line 25 seems to refer back to "a device set Z(CX_H)" in line 25. If this is true, it is suggested to change "device set Z(CX_H)" in line 25 to --the device set Z(CX_H)--. In line 17, "the path R is **in** a member of the sequence cx" seems to be a typographical error. If this is true, it is suggested to delete "in". In line 22, "the device plurality Z" seems to refer back to "a device set Z" in line 8. If this is true, it is suggested to change "the device plurality Z" to --the device set Z--.

Claim 52, "paths P(x)" in line 3 seems to refer back to "paths P(x)" in line 29 of claim 51. If this is true, it is suggested to change "paths P(x)" in line 3 to --the paths P(x)--.

Claim 55, "device set Z(D)" in line 11 seems to refer back to "device set Z(D)" in line 9. If this is true, it is suggested to change "device set Z(D)" to --the device set Z(D)--.

Claim 57, line 10, it is suggested to change "NF" to --N_F--. In line 15, it is suggested to change "message M" to --the message M--.

Claim 58, line 3, it is suggested to insert -- the nodes-- before "N_F and N_E.".

Claim 63, line 12, it is suggested to insert --the-- before "node NA".

Claim 67, "distinct nodes N_A and N_F " in line 3 seems to refer back to "distinct nodes N_A and N_F " in line 9 of claim 57. If this is true, it is suggested to change "distinct nodes N_A and N_F " to --the distinct nodes N_A and $N_{F^{--}}$. "a message M_A and a message M_F " in line 4 seems to refer

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back to "a message M_A " and "a message M_F " in lines 11 and 12 of claim 57. If this is true, it is suggested to change "a message M_A and a message M_F " to --the message M_A and the message M_{F} --.

Claim 69, line 3, it is suggested to insert -- the nodes-- before "N_F and N_E.".

Claim 74, line 12, it is suggested to insert --the-- before "node NA".

Claim 76, line 4, it is suggested to delete "I".

Claim 78, "distinct nodes N_A and N_F " in line 3 seems to refer back to "distinct nodes N_A and N_F " in line 9 of claim 68. If this is true, it is suggested to change "distinct nodes N_A and N_F " to --the distinct nodes N_A and $N_{F^{--}}$.

Claim 79, it is suggested to insert --the-- before "device set A" in line 6; "node set T" in lines 7, 9; "device set Z" in line 8; "path set P" in lines 10, 12; "node N_D " and "node N_E " in line 22; "device set $Z(N_A)$ " in line 23.

Claim 88, line 12, it is suggested to insert --the-- before "node N_A".

Claim 98, it is suggested to insert --the-- before "device set A" in line 6; "node set T" in line 7 and 9; "device set Z" in line 8; "interconnect set I" in line 16; "node N_A" in line 21.

Claim 107, line 12, it is suggested to insert --the-- before "node N_A".

Claim Rejections - 35 USC § 112

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

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4. Claims 39-78, 85, 88, 92, and 98-116 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

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Claim 39 recites the limitation "the sequence cq" in line 23. There is insufficient antecedent basis for this limitations in the claim.

Claim 42 recites the limitation "the level N node set L_N " in line 7. There are insufficient antecedent basis for these limitations in the claim.

Claim 44 recites the limitation "the level L_{N-1} " in line 5. There are insufficient antecedent basis for these limitations in the claim.

Claim 46 recites the limitation "the node pis" in line 5. There are insufficient antecedent basis for these limitations in the claim. It is not clear what the applicant means by "pis".

Claim 51 recites the limitation "the sequence cq" in line 23. There is insufficient antecedent basis for this limitation in the claim.

Claim 53 recites the limitation "the level N node set L_N " in line 7. There are insufficient antecedent basis for these limitations in the claim.

Claim 55 recites the limitation "the level L_{N-1} " in line 5. There are insufficient antecedent basis for these limitations in the claim.

Claim 57, line 9, "the nodes ill" lack antecedent basis and unclear. "the message M_F in line 11 and "the message M_A " in line 12 lack antecedent basis.

Claim 60 recites the limitation "the individual nodes" in line 4. There are insufficient antecedent basis for these limitations in the claim.

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Claim 62, "the plurality of control interconnect paths" in line 9 and "the logic L_A " in line 13 lack antecedent basis.

Claim 63, "the nodes ... N_D " in line 3 and "the logic element L_A " in line 12 lack antecedent basis.

Claim 64, "the plurality of control signal interconnect paths" in line 9 lack antecedent basis.

Claim 68, "the message M_F " in line 11 and "the message M_A " in line 12 lack antecedent basis.

Claim 71 recites the limitation "the individual nodes" in line 4. There are insufficient antecedent basis for these limitations in the claim.

Claim 73 recites the limitation "the plurality of control interconnect paths" in line 9 and "the logic L_A " in line 13. There are insufficient antecedent basis for these limitations in the claim.

Claim 74, "the nodes ... N_D " in line 3 and "the logic element L_A " in line 12 lack antecedent basis.

Claim 75 recites the limitation "the plurality of data-carrying interconnect paths" in line 6 and "the plurality of control signal interconnect paths" in line 9. There are insufficient antecedent basis for these limitations in the claim.

Claim 85 recites the limitation "the plurality of data interconnect paths" in line 6, "the plurality of control interconnect paths" in line 9, and "the logic L_A" in line 13. There are insufficient antecedent basis for these limitations in the claim.

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Claim 88 recites the limitation "the interconnect paths" in line 4, "the plurality of interconnect paths" in line 9, and "the logic element L_A" in line 12. There are insufficient antecedent basis for these limitations in the claim.

Claim 92 recites the limitation "the plurality of data-carrying interconnect paths" in line 7 and "the plurality of control signal interconnect paths" in line 9. There are insufficient antecedent basis for these limitations in the claim.

Claim 98 recites the limitation "the device set $Z(L_{AE})$ " in line 23 and "the device subset $Z(L_{AD})$ " in line 24. There are insufficient antecedent basis for these limitations in the claim.

Claim 104 recites the limitation "the plurality of data interconnect paths" in line 6 and "the plurality of control interconnect paths" in line 9. There are insufficient antecedent basis for these limitations in the claim. It is suggested to insert --the-- before "plurality of control interconnect paths" in line 13.

Claim 107 recites the limitation "the interconnect paths" in line 4 and "the plurality of interconnect paths" in line 9 and "the logic element L_A" in line 14. There are insufficient antecedent basis for these limitations in the claim. It is suggested to insert --the-- before "plurality of control interconnect paths" in line 13.

Claim 111 recites the limitation "the plurality of data-carrying interconnect paths" in line 7 and "the plurality of control signal interconnect paths" in line 10. There are insufficient antecedent basis for these limitations in the claim. It is suggested to insert --the-- before "plurality of control interconnect paths" in line 13.

Allowable Subject Matter

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5. Claims 39-116 would be allowable if rewritten or amended to overcome the objection(s) and/or the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Lee et al (5,224,100), Teacherson (6,578,010), Horstmann et al (5,774,369), Born Eng (5,781,551), and Kaye Stefan (5,694,393).

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian D Nguyen whose telephone number is (571) 272-3084. The examiner can normally be reached on 7:30-6:00 Monday-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chau Nguyen can be reached on (571) 272-3126. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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2/15/05

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